

Listing of Claims:

Please cancel claims 2-27.

1. (Original) A single integrated circuit comprising
a plurality of logic elements (LEs) for generating a plurality of output signals in response to a plurality of input signals correspondingly applied to the LEs, each of the LEs being equipped to hold constant the LE's output signal on demand; and

a context bus, including associated read/write control facilities, coupled to the LEs for outputting individual signal state values of the LEs out of the integrated circuit, and for initializing the LEs with individual signal state values provided to the integrated circuit while the output signals of the LEs are being held constant.

Claims 2-27 (Canceled).